

AMENDMENTS TO THE CLAIMS:

Complete Listing of Claims

Claim 1 (canceled)

Claim 2 (canceled)

1 Claim 3. (currently amended) An arrangement for dynamically clamping
2 overshoot in a received signal on a signal line while not clamping the received
3 signal at times when the overshoot is not likely encountered, the arrangement
4 comprising:
5 an activation element configured to assert a Clamp Enable signal in
6 response to a transition of the received signal;
7 a deactivation element configured to assert a Clamp Disable signal after
8 the Clamp Enable signal is asserted; and
9 a clamping portion configured to clamp the received signal beginning with
10 the assertion of the Clamp Enable signal and ending with the assertion of the
11 Clamp Disable signal ~~The arrangement of Claim 2, wherein:~~
12 the activation element is configured to assert the Clamp Enable signal in
13 response to the transition of the received signal that occurs during a period in
14 which a Driver Disable signal is asserted.

1 Claim 4. (original) The arrangement of Claim 3, wherein:
2 the deactivation element includes a deactivation delay that is configured
3 to assert a Clamp Disable signal a predetermined deactivation delay period after
4 the Clamp Enable signal is asserted.

1 Claim 5. (original) The arrangement of Claim 4, wherein:

2 Driver Disable signal is provided to the arrangement on a circuit card that
3 also includes an output driver whose output is disabled by the Driver Disable
4 signal to present a high-impedance state to the signal line so that the output
5 driver does not affect the received signal on the signal line.

1 Claim 6. (original) The arrangement of Claim 5, wherein the activation element
2 includes stacked first and second pairs of metal oxide semiconductor field effect
3 transistors (MOSFETs) that collectively perform a logical AND function on the
4 Driver Disable signal and the received signal on the signal line to form the Clamp
5 Enable signal, wherein:

6 the first pair of MOSFETs is responsive to the Driver Disable signal; and
7 the second pair of MOSFETs is responsive to the received signal on the
8 signal line.

1 Claim 7. (original) The arrangement of Claim 6, wherein the activation element
2 further includes:

3 an activation delay arrangement configured to delay an output of the first
4 and second stacked pairs of MOSFETs so as to form the Clamp Enable signal.

1 Claim 8. (original) The arrangement of Claim 5, wherein:

2 the clamping portion includes a diode and a switching element connected
3 in series between a voltage supply and the signal line;

4 the switching element is responsive to the Clamp Enable signal to enable
5 the diode to clamp overshoot on the signal line; and

6 the switching element is responsive to the Clamp Disable signal to disable
7 the diode from clamping the signal line.

1 Claim 9. (original) The arrangement of Claim 8, wherein the clamping portion
2 further includes:

3 at least one switching element configured to ensure that the clamping
4 portion does not clamp the signal line when a received signal on the signal line
5 exceeds the voltage supply.

1 Claim 10. (original) The arrangement of Claim 8, wherein the clamping portion
2 further includes:

3 at least one switching element configured to ensure that the clamping
4 portion does not clamp the signal line when the voltage supply is zero.

Claim 11 (canceled)

Claim 12 (canceled)

1 Claim Claim 13. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 3.

1 Claim 14. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 4.

1 Claim 15. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 5.

1 Claim 16. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 6.

1 Claim 17. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 7.

1 Claim 18. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 8.

1 Claim 19. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 9.

1 Claim 20. (original) A backplane-insertable circuit card that includes the
2 arrangement of Claim 10.

Claim 21 (canceled)

1 Claim 22. (original) A method of dynamically clamping overshoot in a received
2 signal on a signal line while not clamping the received signal at times when the
3 overshoot is not likely encountered, the arrangement comprising:

4 asserting a Clamp Enable signal in response to a transition of the
5 received signal;

6 asserting a Clamp Disable signal after the Clamp Enable signal is
7 asserted; and

8 clamping the received signal beginning with the assertion of the Clamp
9 Enable signal and ending with the assertion of the Clamp Disable signal ~~The~~
10 ~~method of Claim 21~~, wherein the step of asserting the Clamp Enable signal
11 includes:

12 asserting the Clamp Enable signal in response to a transition of the
13 received signal that occurs during a period in which a Driver Disable signal is
14 asserted.

1 Claim 23. (original) The method of Claim 22, wherein the step of asserting the
2 Clamp Disable signal includes:

3 asserting the Clamp Disable signal a deactivation delay period after the
4 Clamp Enable signal is asserted.

1 Claim 24. (original) The method of Claim 23, further comprising:

2 ensuring that the received signal is not clamped when a voltage supply is
3 zero.

- 1 Claim 25. (original) The method of Claim 23, further comprising:
- 2 ensuring that the received signal is not clamped when the received signal
- 3 exceeds a non-zero voltage supply.